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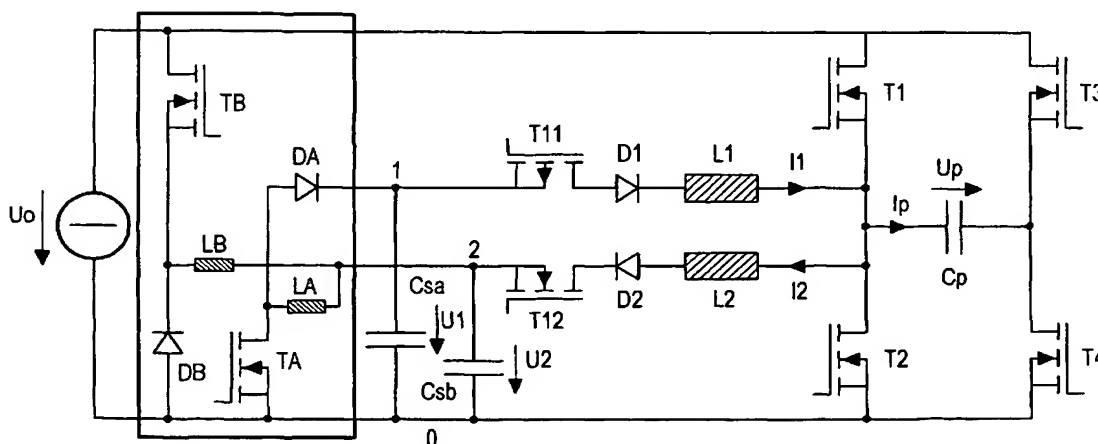
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(54) Title: **CIRCUIT ARRANGEMENT FOR THE AC POWER SUPPLY OF A PLASMA DISPLAY PANEL**



(57) Abstract: A circuit arrangement for commutating the AC voltage of a plasma display panel, in which the losses and electro-magnetic interference which occur as a result of the influence of parasitic resistances (coils, supply lines, semiconductor switches) and the attendant hard charging and discharging processes, are substantially avoided. For the charging operation of the capacitor (Cp) of the plasma cells the oscillation circuit is supplied with an auxiliary charging voltage (u1) whose value exceeds 50% of the input voltage (U0). The oscillation circuit for the discharging operation is supplied with an auxiliary discharging voltage (u2) whose value falls short of 50% of the input voltage (U0). The two auxiliary voltages are connected to a DC converter.